

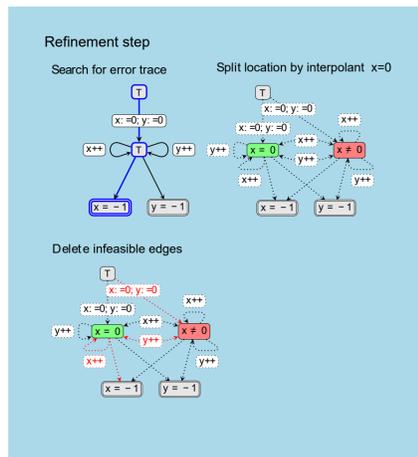
ULTIMATE KOJAK

Features

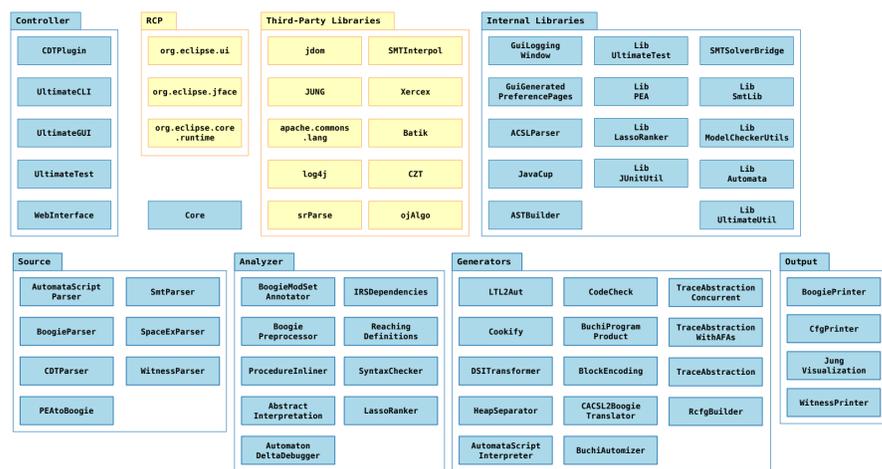
- Reachability analysis
- Memory safety analysis
- Bitprecise analysis
- IEEE 754 floating point analysis
- Error witnesses
- Correctness witnesses

Techniques

- Abstraction refinement
- Configurable block encodings
- Multi SMT solver support
- Newton-style interpolation



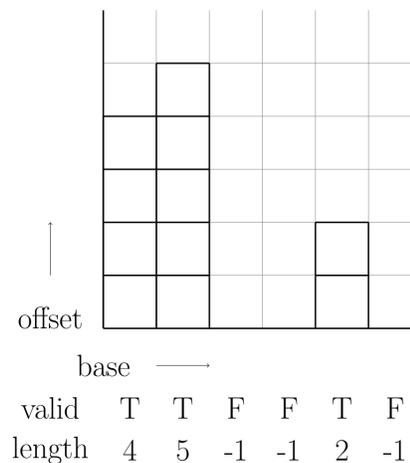
ULTIMATE program analysis framework



C memory model

Models dynamically allocated memory through Boogie arrays:

- **memory-[int|pointer|bitvector8|...]**: store memory contents
 - one array per used Boogie data type
 - two dimensional, a memory address has components “base” and “offset”
 - models disjointness of memory areas allocated by different malloc calls
- **valid**: store which base addresses are allocated
- **length**: store maximal offset at each base address
- “*p is a valid pointer dereference” $\iff \text{valid}[p.\text{base}] \wedge p.\text{offset} \leq \text{length}[p.\text{base}]$
- “Program has no memory leaks” $\iff \text{valid} = \text{old}(\text{valid})$ at the end of main



SMT solver integration

Hoare triple checks

“Is $\{P\} s \{Q\}$ a Hoare triple?”

Features:

- Simplify check if $(\text{variables}(P) \cup \text{variables}(st)) \cap \text{variables}(Q) = \emptyset$.
 - often blocked because P , st and Q access the same array (but perhaps at different positions)
 - attempt to partition arrays via “alias analysis” (work in progress)
- Avoid checks with intricate predicates.
- Use incremental (push/pop) solver queries when possible, e.g., group checks that share the same precondition P .
- Abstract interpretation-based: Check if $\text{post}^\#(P^\#, st) \sqsubseteq Q^\#$ holds in some abstract domain.
- Unify equivalent predicates.
- Cache Hoare triples and implication between predicates.

Tree interpolation

- Interpolating solvers used by Ultimate: SMTInterpol, Z3
- Tree interpolation syntax example (procedures `foo`, `bar`):

```
(assert (! (..) :named foo-stm1))
(assert (! (..) :named foo-stm2))
(assert (! (..) :named bar-stm1))
(assert (! (..) :named bar-stm2))
(assert (! (..) :named foo-stm3))
(check-sat)
(get-interpolants (foo-stm1 foo-stm2 (bar-stm1 bar-stm2) foo-stm3))
```

Interface

- Java interface (currently only SMTInterpol)
- SMTLib2 interface
- Solvers in use at SV-COMP 2018: SMTInterpol, Z3, MathSat, CVC4 as many as we can get!

Newton-style interpolation

- Input: infeasible trace $\mathcal{S}_1, \dots, \mathcal{S}_n$, unsatisfiable core $UC \subseteq \{\mathcal{S}_1, \dots, \mathcal{S}_n\}$
- Replace statements not in UC:
 - assume statement $\psi \rightsquigarrow \text{skip}$
 - assignment statement $x:=t \rightsquigarrow \text{havoc } x$
- Compute sequence of predicates $\varphi_0, \dots, \varphi_n$ iteratively using strongest post operator post
 - $\varphi_0 := \text{true}$
 - $\varphi_{i+1} := \text{post}(\varphi_i, \mathcal{S}_{i+1})$
- Eliminate each variable from predicate φ_i that is not live at position i of the trace.
- Output: sequence of predicates $\varphi_0, \dots, \varphi_n$ which is a sequence of interpolants for the infeasible trace $\mathcal{S}_1, \dots, \mathcal{S}_n$

trace	state assertions for τ	interpolating trace $\tau^\#$	state assertions for $\tau^\#$
	φ_0 true		φ_0 true
st_1 <code>b:=a</code>	φ_1 $a = b$	<code>b:=a</code>	φ_1 $a = b$
st_2 <code>x:=0</code>	φ_2 $a = b \wedge x = 0$	<code>havoc x</code>	φ_2 $a = b$
st_3 <code>havoc p</code>	φ_3 $a = b \wedge x = 0$	<code>havoc p</code>	φ_3 $a = b$
st_4 <code>!a[p]</code>	φ_4 $a = b \wedge x = 0 \wedge a[p] = \text{false}$	<code>!a[p]</code>	φ_4 $a = b \wedge a[p] = \text{false}$
st_5 <code>a[p]:=true</code>	φ_5 $a = b[p := \text{true}] \wedge x = 0 \wedge a[p] = \text{true}$	<code>a[p]:=true</code>	φ_5 $a = b[p := \text{true}] \wedge a[p] = \text{true}$
st_6 <code>x:=x+1</code>	φ_6 $a = b[p := \text{true}] \wedge x = 1 \wedge a[p] = \text{true}$	<code>havoc x</code>	φ_6 $a = b[p := \text{true}] \wedge a[p] = \text{true}$
st_7 <code>a[p]:=false</code>	φ_7 $a = b \wedge x = 1 \wedge a[p] = \text{false}$	<code>a[p]:=false</code>	φ_7 $a = b \wedge a[p] = \text{false}$
st_8 <code>a!=b</code>	φ_8 false	<code>a!=b</code>	φ_8 false